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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/830,097	04/23/2004	Yoshiaki Okuno	0925-0179PUS2	7525
2292 7590 10/28/2008 BIRCH STEWART KOLASCH & BIRCH				INER
PO BOX 747 FALLS CHURCH, VA 22040-0747			DINH, DUC Q	
FALLS CHURG	л, vA 22040-0747		ART UNIT PAPER NUMBER	
			NOTIFICATION DATE	DELIVERY MODE
			10/28/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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	Application No.	Applicant(s)	
	10/830,097	OKUNO ET AL.	
Office Action Summary	Examiner	Art Unit	
	DUC Q. DINH	2629	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet wit	h the correspondence address -	-
A SHORTENED STATUTORY PERIOD FOR REWHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the nearned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUNIC R 1.136(a). In no event, however, may a re t. briod will apply and will expire SIX (6) MON tatute, cause the application to become AB	CATION. sply be timely filed IHS from the mailing date of this communica ANDONED (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on 0 2a) This action is FINAL . 2b) Since this application is in condition for all closed in accordance with the practice und	This action is non-final. wance except for formal matte	•	s is
Disposition of Claims			
4) Claim(s) 1-6 is/are pending in the application 4a) Of the above claim(s) is/are with 5) Claim(s) 1-4 is/are allowed. 6) Claim(s) 5-6 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction are Application Papers	drawn from consideration. nd/or election requirement.		
9) The specification is objected to by the Exam 10) The drawing(s) filed on is/are: a) Applicant may not request that any objection to Replacement drawing sheet(s) including the col 11) The oath or declaration is objected to by the	accepted or b) objected to be the drawing(s) be held in abeyan rrection is required if the drawing(ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.12	• •
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the priority docum application from the International Bu * See the attached detailed Office action for a	nents have been received. nents have been received in A priority documents have been reau (PCT Rule 17.2(a)).	oplication No received in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date) Paper No(s	ummary (PTO-413))/Mail Date formal Patent Application 	



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DETAILED ACTION

1. This Office Action is responsive to the Applicant's Response to the Restriction/Election Requirement filed on July 09, 2008. In view of the Applicant's argument in the Remarks, the Restriction/Election Requirement hereby withdrawn. Accordingly, claims 1-6 are currently pending in the Application and being examined as follows.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 5-6 are rejected under 35 U.S.C. 102(e) as being anticipated by Schiefer et al. (U.S. Patent No. 6,177, 922), hereinafter Schiefer.

In reference to claim 5, Schiefer discloses a device (see Figs. 1-3) for converting images, comprising:

an output synchronization signal generation module (120) configured to generate output vertical and horizontal synchronization signals (Hsync2 and Vsync2) based on an input horizontal synchronization signal (RGB Hsyns) and an input vertical synchronization signal (RGB Vsync), and a clock signal (CLK2) that is asynchronous to the input horizontal synchronization signal; and

a controller (110) for generating a control signal for controlling a cycle of the output horizontal synchronizing signal based on the input horizontal synchronization (RGB Hsync) signal, the input vertical synchronization signal (RGB Vsync), cycle information of the input horizontal synchronization signal (IP Hsync), and cycle information of the asynchronous clock signal (IPCLK); and

an image conversion module (320 of Fig. 3) configured to generate an output image signal (Ddata) based on an input image signal (data) the output vertical and horizontal synchronization signals (RGB Vsyn/Hsyn), and the clock signal (CLK2).

In reference to claim 6, Schiefer discloses, in Figs. 1-3, a method for converting images, comprising:

generating output vertical and horizontal synchronization signals (Vsync/Hsync) based on an input horizontal synchronization signal (HS sync) and an input vertical synchronization signal (VS sync);

generating a clock signal (CLK) that is asynchronous to the input horizontal synchronization signal; (HS sync) and

generating a control signal that controls a cycle of the output horizontal synchronizing signal based on the input horizontal synchronization signal (HS sync), the input vertical synchronization signal (VS sync), cycle information of the input horizontal synchronization signal (IP Hsync), and cycle information of the a synchronous clock signal (IPCLOCK); and

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generating an output image signal (DDATA) based on an input image signal (DATA), the output vertical and horizontal synchronization signals (Vsync, Hsyn), and the clock signal (CLK).

Allowable Subject Matter

3. Claims 1-4 are allowed as indicated in the previous Office Action mailed on October 30, 2007.

Response to Arguments

4. Applicant's arguments with respect to claims 5-6 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to DUC Q. DINH whose telephone number is (571)272-7686. The examiner can normally be reached on Mon-Fri from 8:00.AM-4:00.PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD HJERPE can be reached on (571)272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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/Duc Q Dinh/

Primary Examiner, Art Unit 2629